

FIG. 1
EMBEDDED IC TRIMMING SCHEMATIC

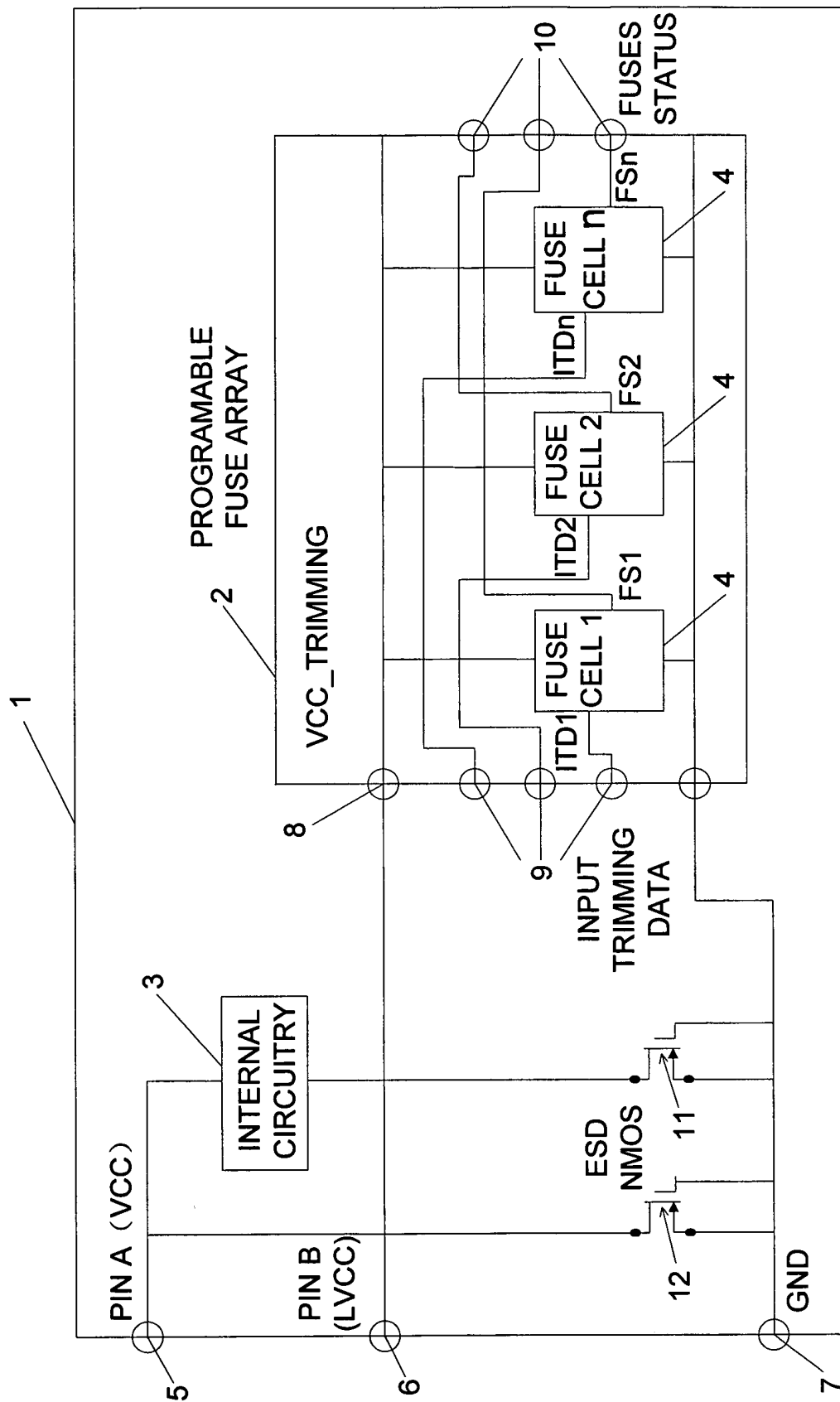


FIG. 2
EMBEDDED IC TRIMMING SCHEMATIC

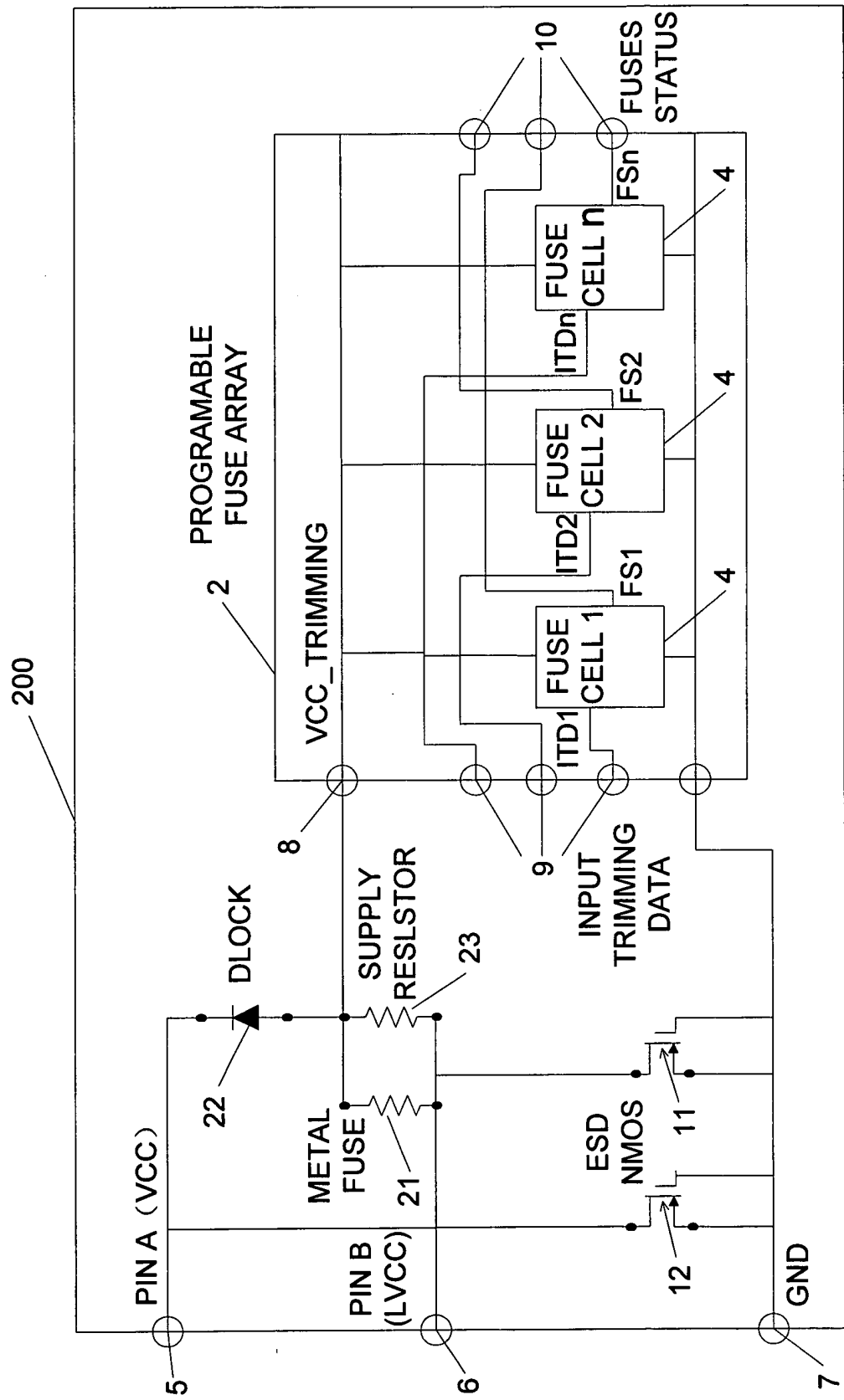


FIG. 3
 EMBEDDED IC TRIMMING LOCKING SCHEMATIC
 TWO POWER SUPPLIES IC

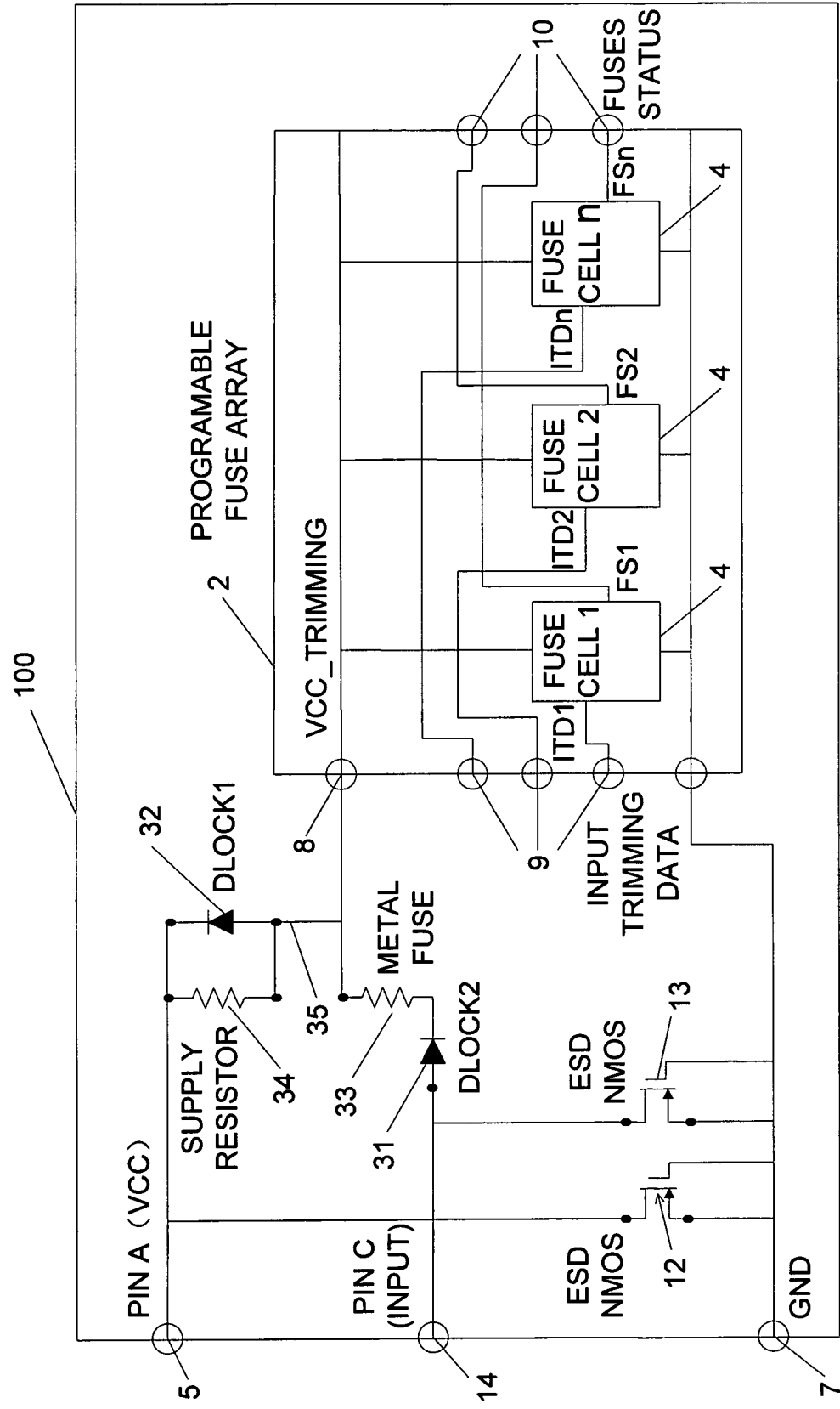


FIG. 4
 EMBEDDED IC TRIMMING LOCKING SCHEMATIC
 ONE VOLTAGE SUPPLY IC

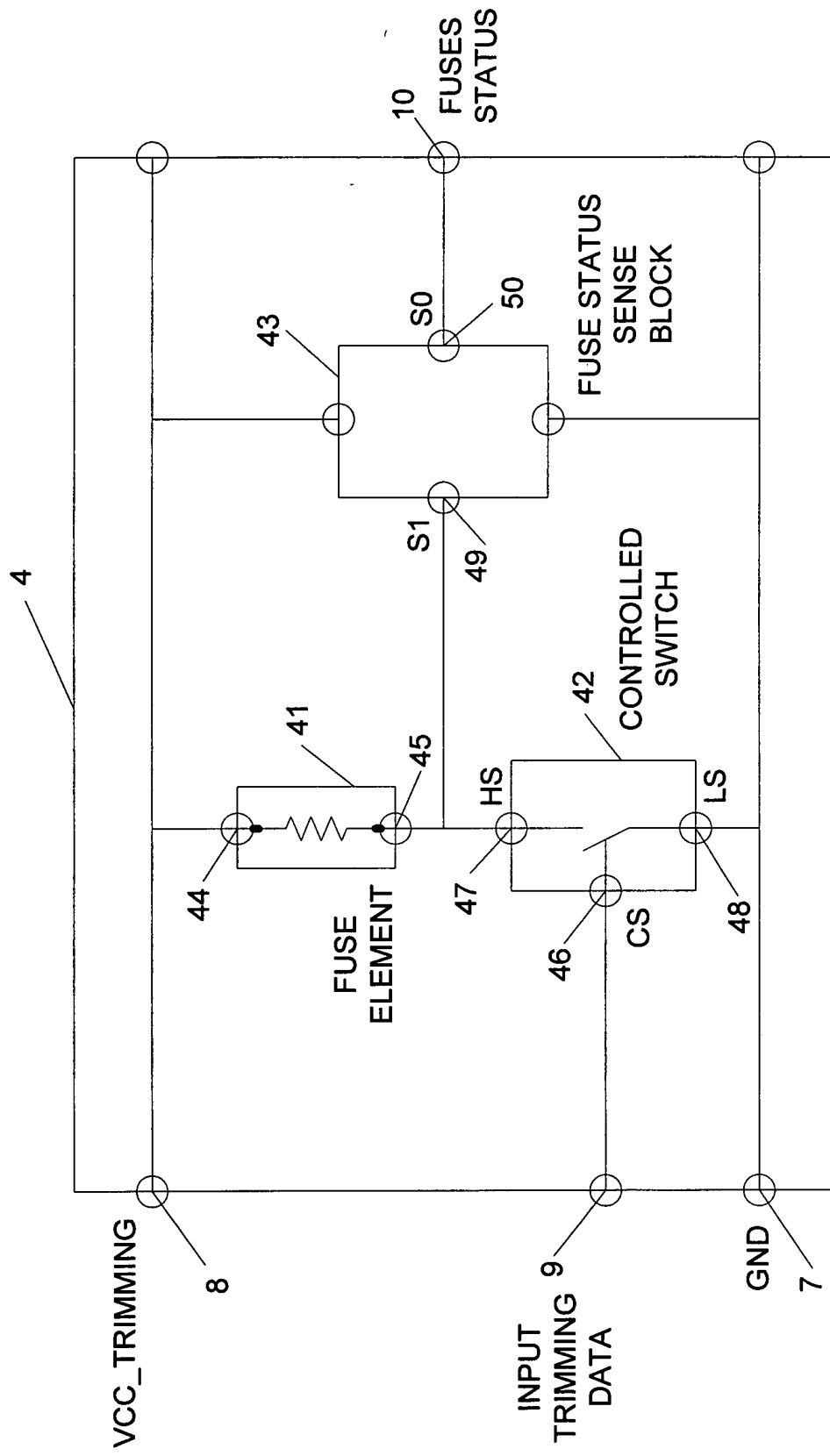


FIG. 5
 PROGRAMMABLE FUSE ARRAY
 ONE CELL ONLY IS SHOWN